

Sheet 1 of 4

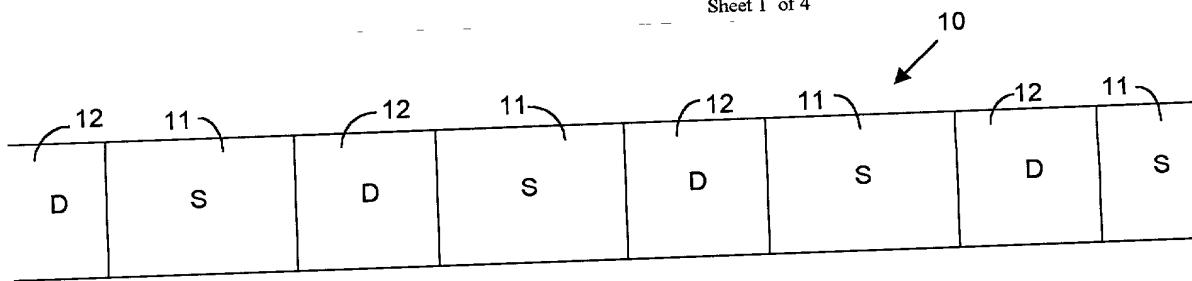


FIG. 1A

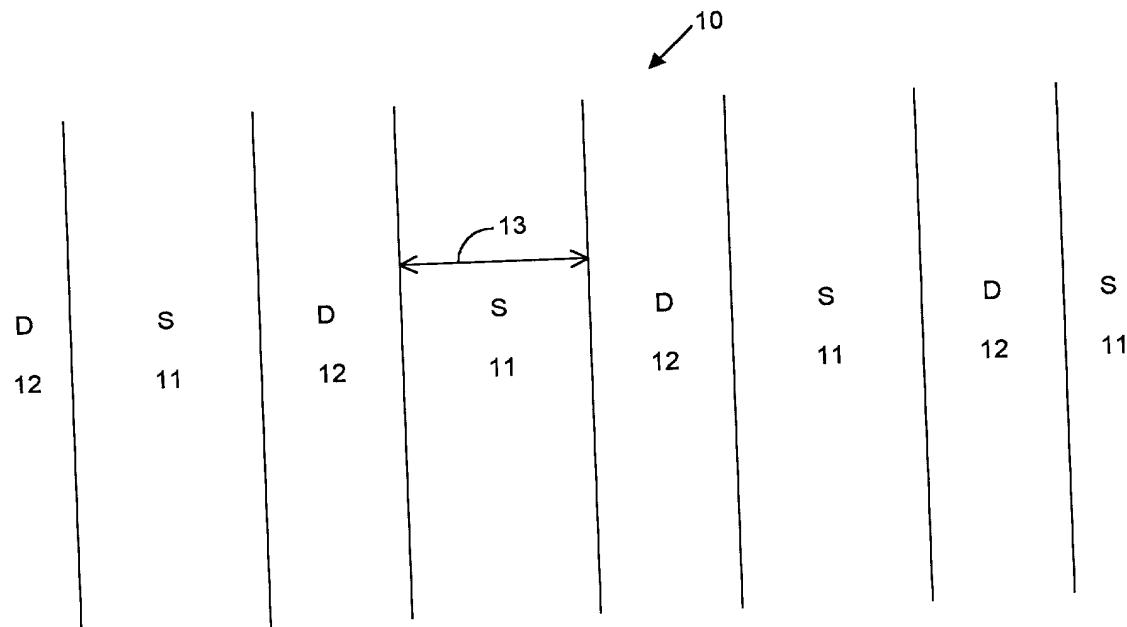


FIG. 1B

FIG. 2A

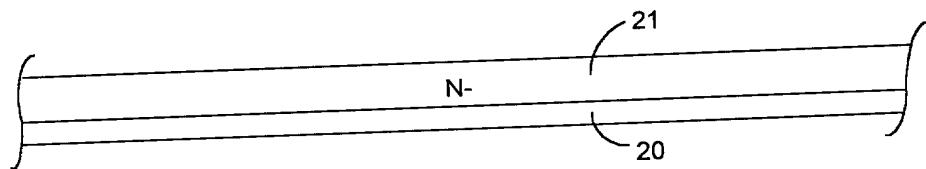


FIG. 2B

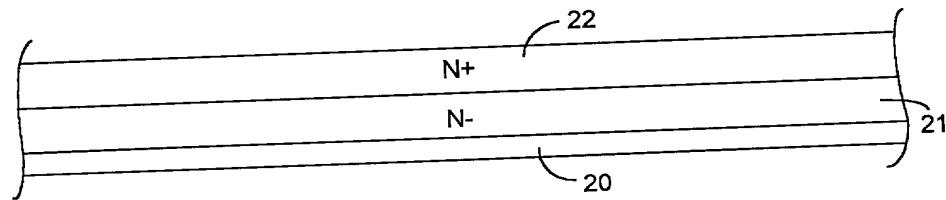


FIG. 2C

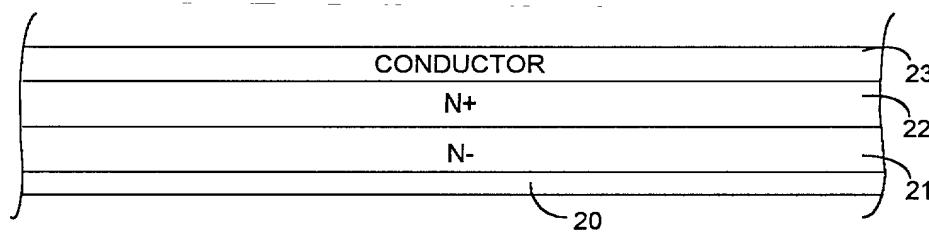


FIG. 2D

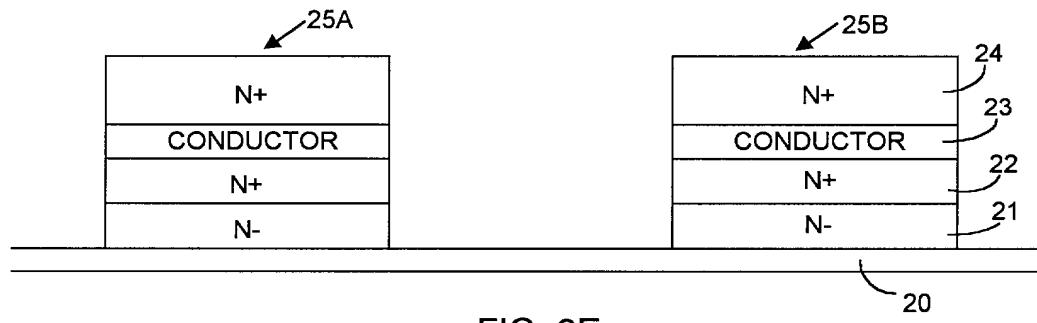
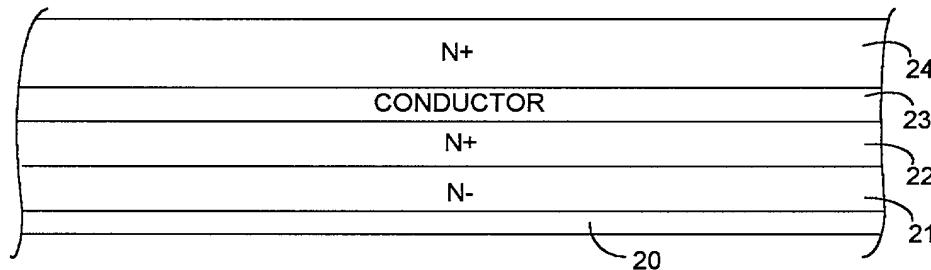


FIG. 2E

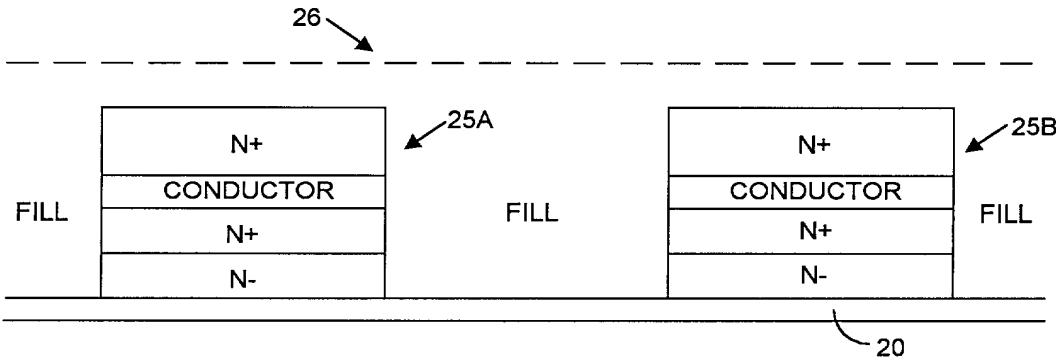


FIG. 2F

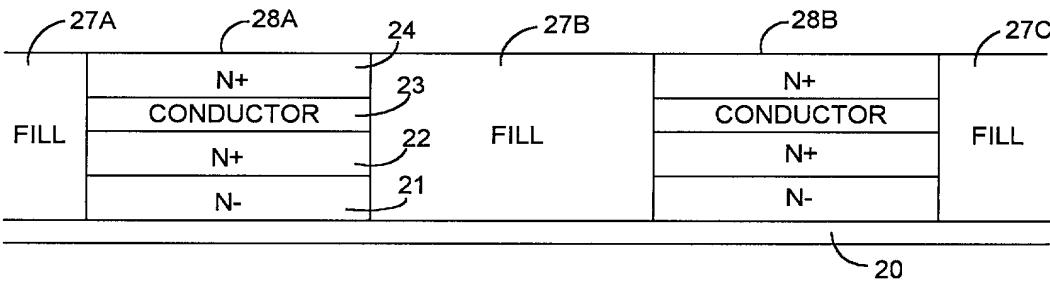
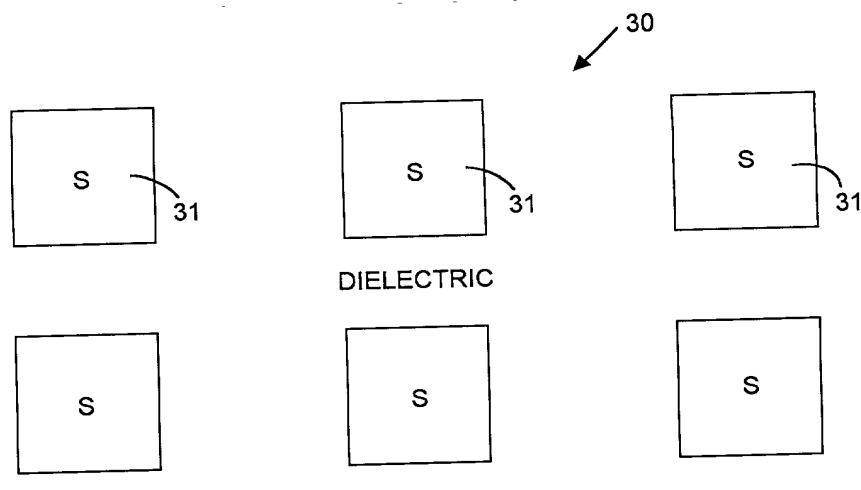
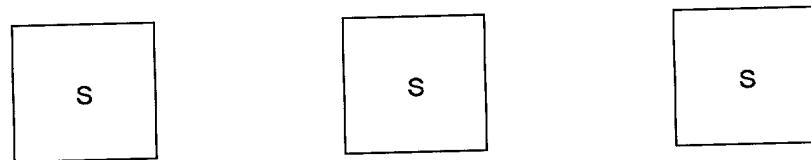


FIG. 2G

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DIELECTRIC



DIELECTRIC

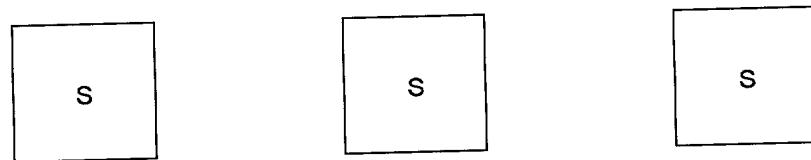
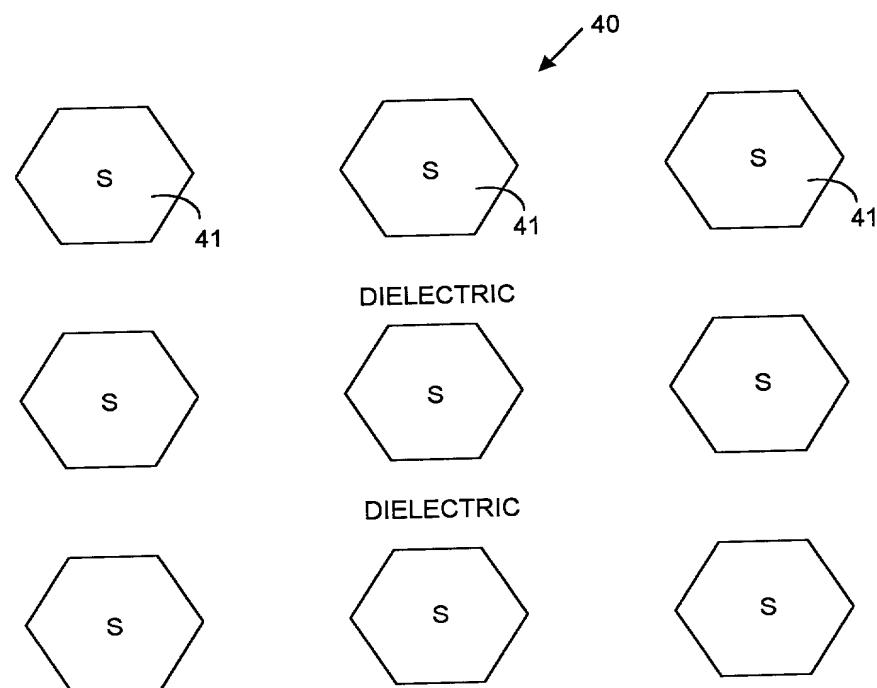
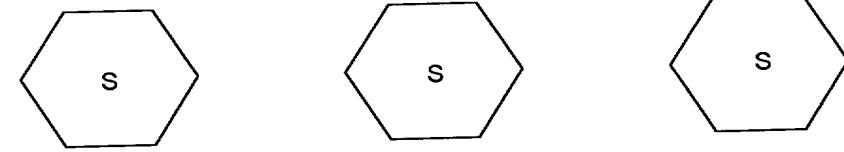


FIG. 3



DIELECTRIC



DIELECTRIC

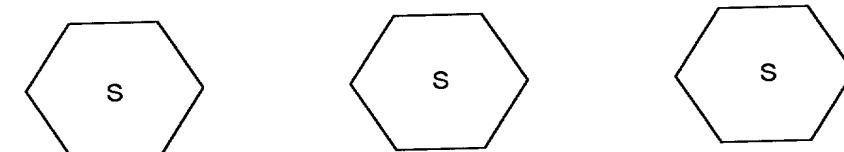


FIG. 4

Applicants: Vyvoda et al.  
Serial No : To Be Assigned  
Filed : Herewith  
For : WAFER SURFACE THAT  
FACILITATES PARTICLE REMOVAL

MSI-1  
Sheet 4 of 4

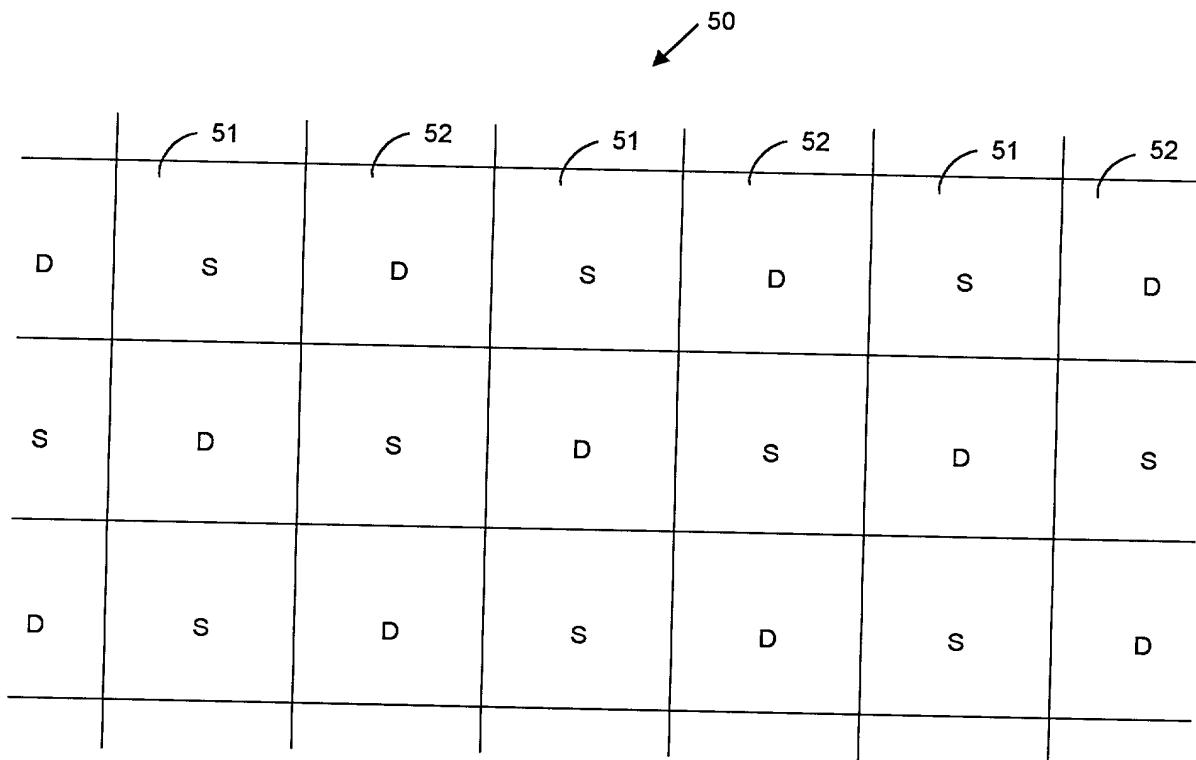


FIG. 5